

<b>Notice of References Cited</b>	Application/Control No. 10/768,724	Applicant(s)/Patent Under Reexamination LIU ET AL.	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,764,949	07-2004	Bonser et al.	438/666
	C	US-2002/0001778	01-2002	Latchford et al.	430/313
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	K	US-			
	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

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	U	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 244-245, 249-250, 639, 679-680.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.